

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Tim Tuan et al.

Assignee: Xilinx, Inc.

Title: "Tuning Programmable Logic Devices for Low-Power Design  
Implementation"

Serial No.: 10/783,216

Filing Date: 02/20/2004

Examiner: Nelson C. Lam

Art Unit: 2825

Docket No.: X-1462-1P US

Conf. No.: 6678

-----  
Mail Stop AF  
COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION**

*NOT  
EST  
J. 11/26/06*  
In response to the final rejection in the Second Office Action mailed from the Patent Office on September 11, 2006, Applicants submit the following amendments and remarks.

**Amendments to the Claims begin on page 2 of this paper.**

**Remarks begin on page 7 of this paper.**